

FIG. 1

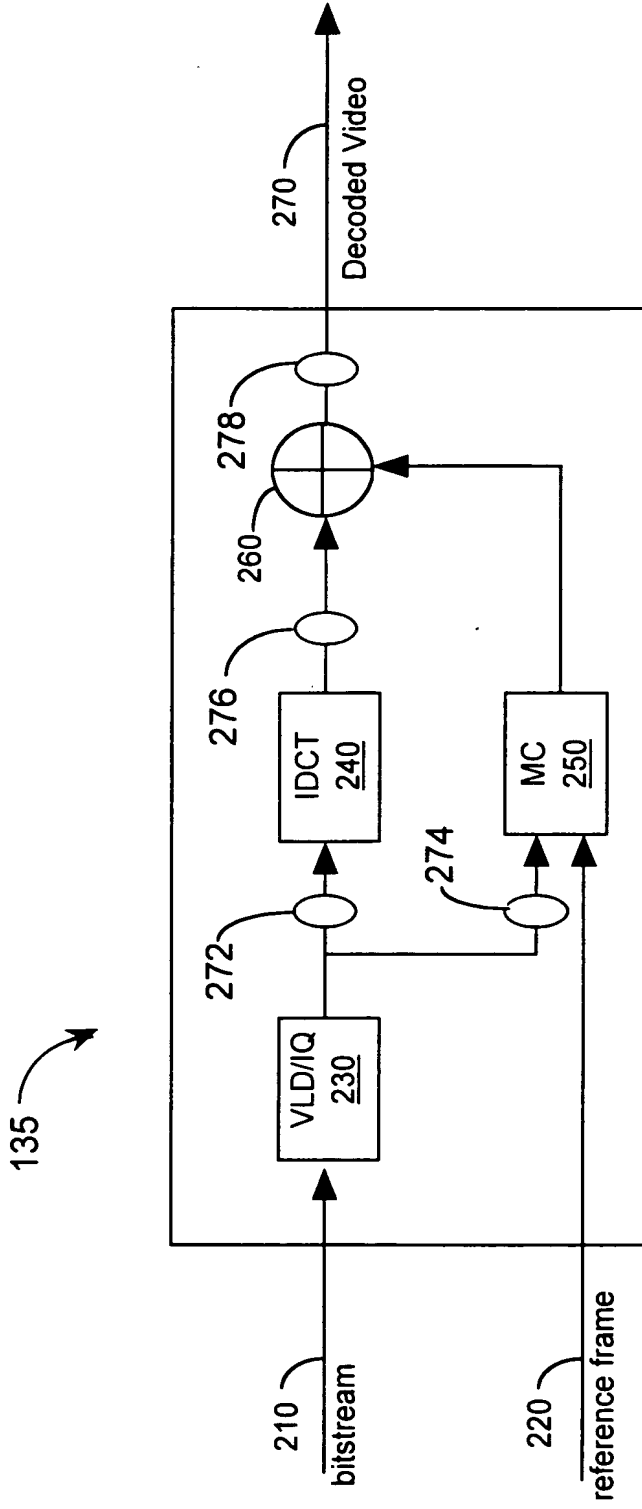


FIG. 2

300



<u>310A</u>			
			<u>310N</u>

FIG. 3

I-cache
Miss-rate

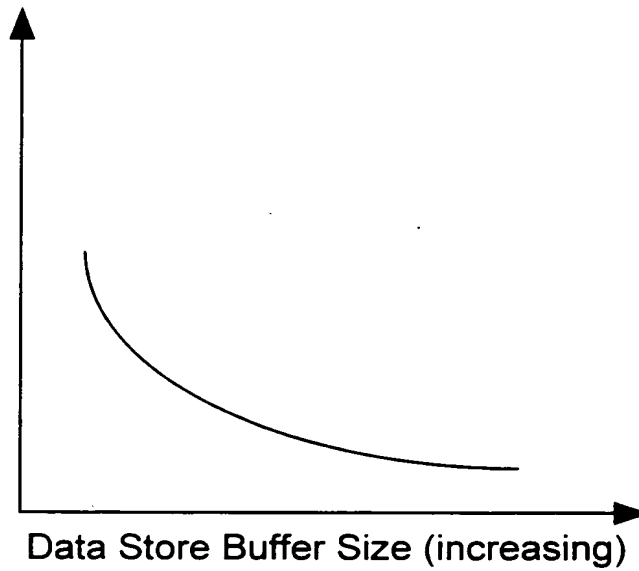
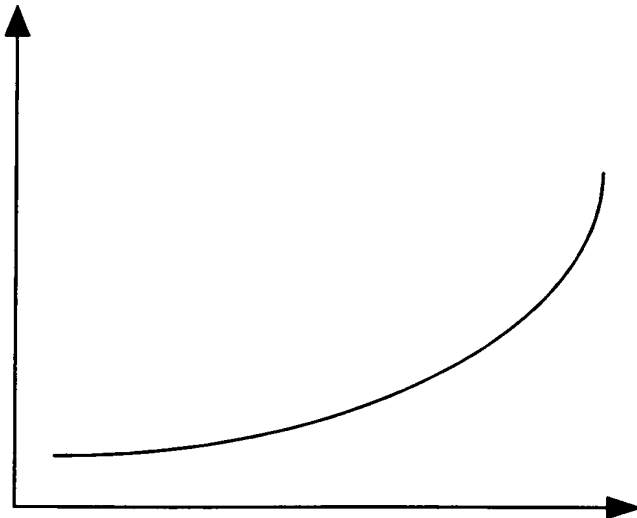


FIG. 4a

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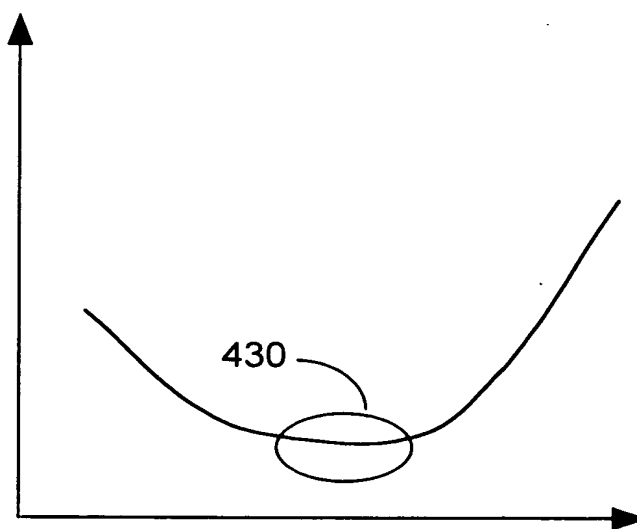
D-cache
Miss-rate



Data Store Buffer Size (increasing)

FIG. 4b

Overall
Cache
Miss-rate



Data Store Buffer Size (increasing)

FIG. 4c

